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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10092526	FILING DATE 03/08/2002	CLASS 257	SUBCLASS 312	GAU 2815	EXAMINER <i>Brock</i>
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\*\*APPLICANTS: Shigematsu Hisao; Imanishi Kenji; Tanaka Hitoshi;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/191,543 11/13/1998 now U.S. PAT. no. 6399971

*HBK*

\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 10-118466/1998 04/28/1998

*HBK*

*yes*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 981308A
Verified and Acknowledged Examiner's initials <i>HBK</i>			
TITLE : Semiconductor device and method for fabricating the same			
U.S.DEP'T. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

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NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
TERMINAL		Primary Examiner		
DISCLAIMER		Application Examiner		
PREPARED FOR ISSUE				
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